CLAIMS:

1. An amplifier circuit for amplifying an input signal, comprising:

a) an input terminal for inputting said input signal, and

b) an output terminal **(RFP, RFN)** for outputting an output signal corresponding to the amplified input signal, and

c) a stray feedback element **(C_P, C_N)** by which said output signal is fed back to said input terminal,

characterized by

d) a feedback compensation terminal **(RFB)** for outputting a predetermined fraction of said output signal so as to reduce the stray feedback of said output signal.

2. An amplifier circuit according to claim 1,

characterized in that

said output terminal is a differential output terminal comprising a first **(RFP)** and a second **(RFN)** output terminal, and said output signal is a differential output signal.

3. An amplifier circuit according to claim 2,

characterized in that

said predetermined fraction is determined by the following equation:

$U_{RFB} = \alpha (U_{RFP} - U_{RFN})$; $-1 < \alpha < 1$

wherein the term $(U_{RFP} - U_{RFN})$ denotes the voltage value of said differential output signal, α denotes the value of said predetermined fraction, and U_{RFB} denotes the voltage value of said predetermined fraction of said output signal.

4. An amplifier circuit according to any one of the preceding claims,

characterized in that

said stray feedback element is a stray capacity **(11, 12)**.

5. An amplifier circuit according to any one of the preceding claims,

characterized in that

an adjustable voltage dividing means **(VD)** is provided for generating said fraction of said output signal.

6. An amplifier circuit according to any one of the preceding claims, **characterized in that** said amplifier circuit is a transimpedance amplifier **(20).**

7. A reproducing device comprising an amplifier circuit **(20)** as claimed in any one of the preceding claims, a reproducing element **(10)** for generating said input signal, channel decoding and/or error correction means **(30)** coupled to an output of said amplifier circuit (20).

8. A reproducing device according to claim 7, wherein said reproducing device is an optical disc player and said reproducing element is a photo diode **(10).**

9. A method for reducing stray feedback in an amplifier circuit **(20),** comprising the steps of :

a) inputting an input signal into an input terminal; and

b) outputting an output signal at an output terminal **(RFP, RFN),**

characterized by the steps of

c) providing a feedback compensation terminal **(RFB)** at said amplifier circuit; and

d) generating a predetermined fraction of said differential output signal and supplying said predetermined fraction to said feedback compensation terminal **(RFB)** so as to reduce stray feedback of said output signal.

10. A method according to claim 9, **characterized by** determining and adjusting the value of said predetermined fraction when said amplifier circuit is manufactured.

11. A method according to claim 9 or 10, **characterized by** updating the value of said predetermined fraction during a start-up of a device in which said amplifier circuit is provided.

12. A method according to any one of claims 9 to 11,

characterized by

generating said predetermined fraction by a voltage dividing operation.